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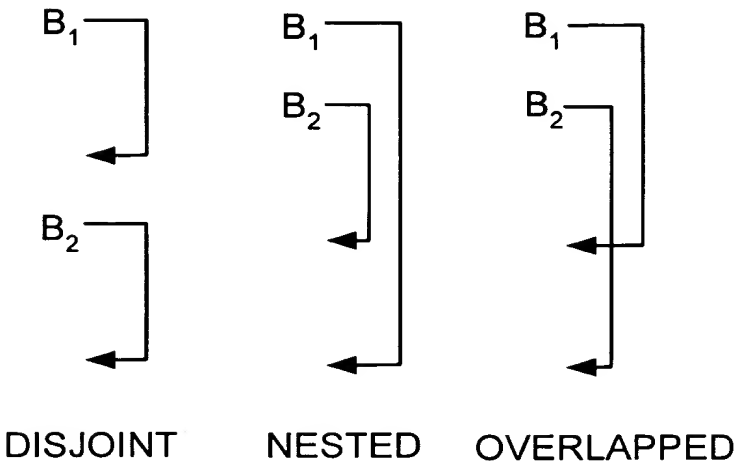
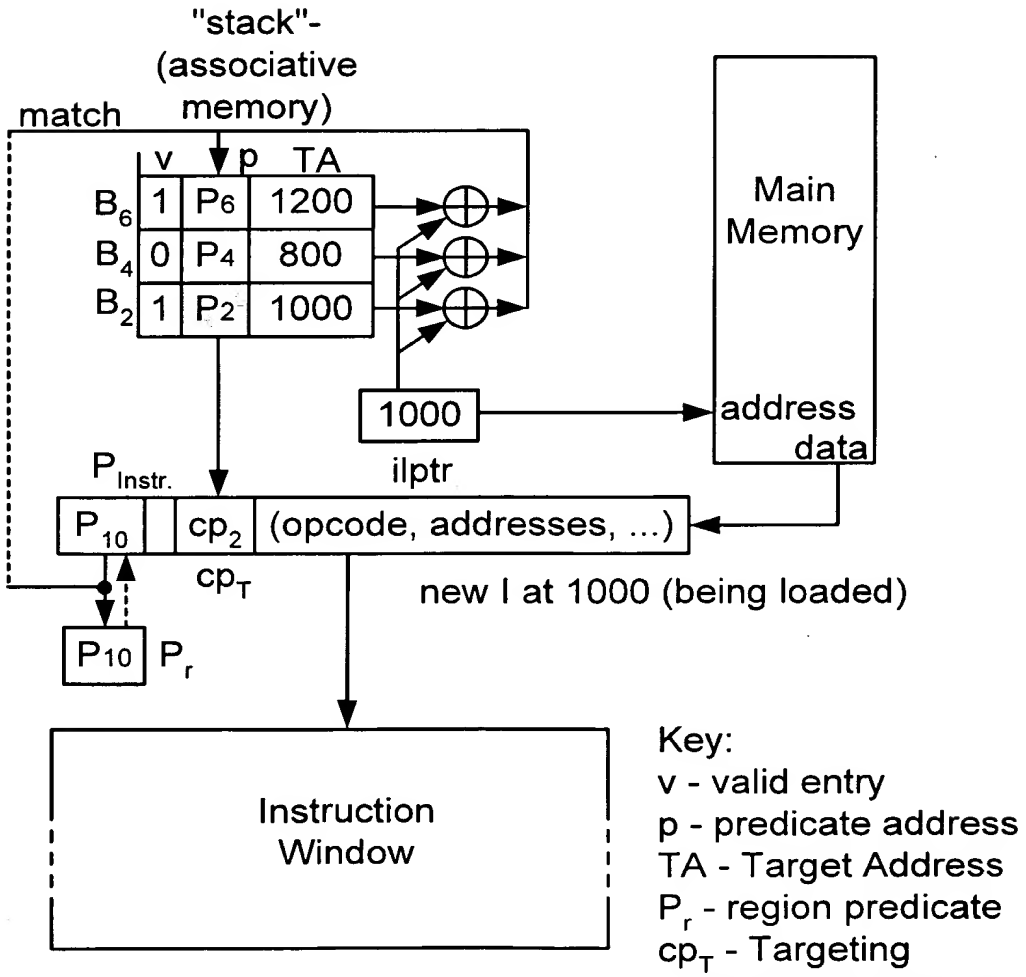


FIG. 1



Snapshot taken at t = 9+ of Example 5.
-new I matches target address in stack

FIG. 2

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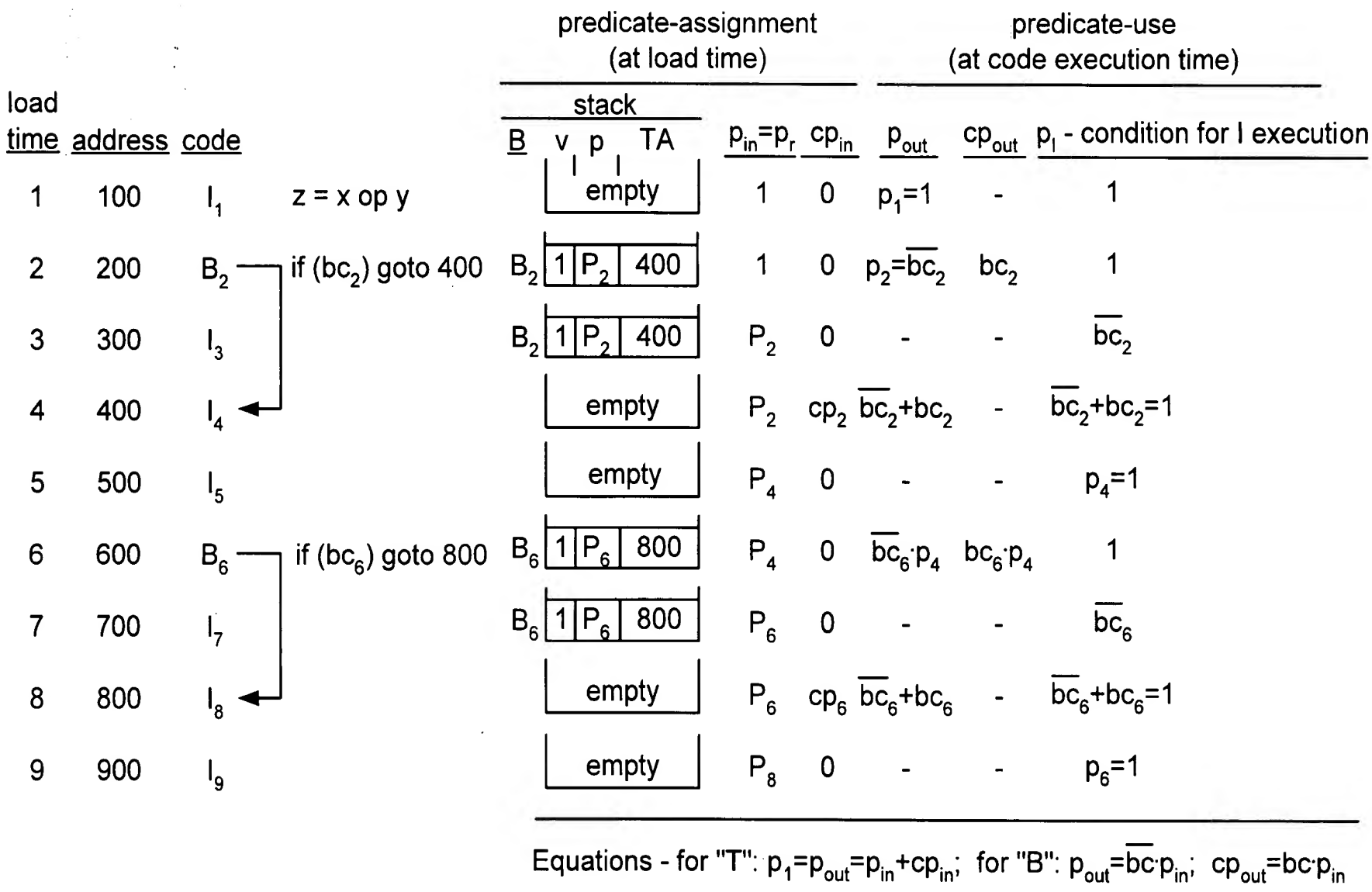

FIG. 3

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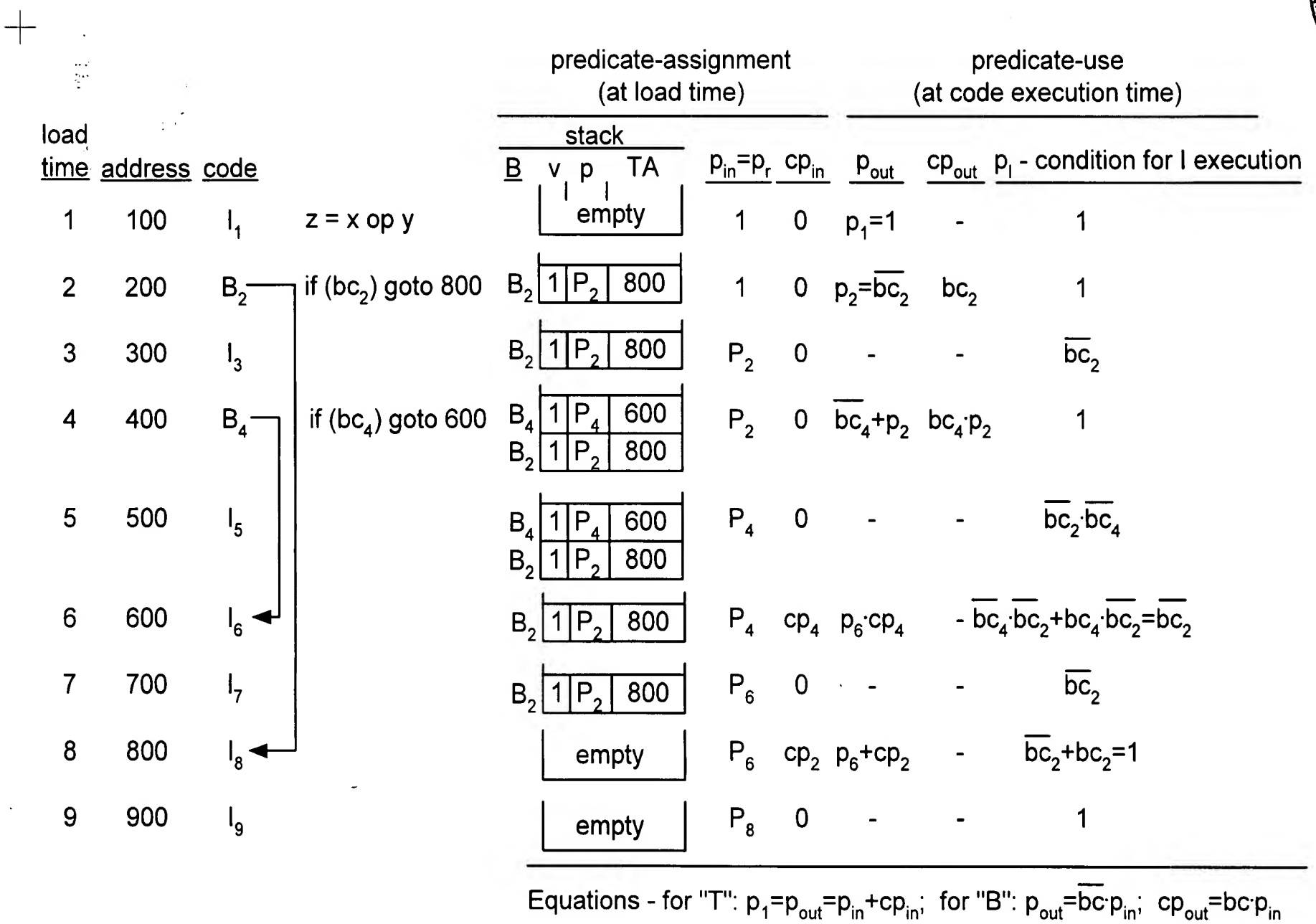

FIG. 4

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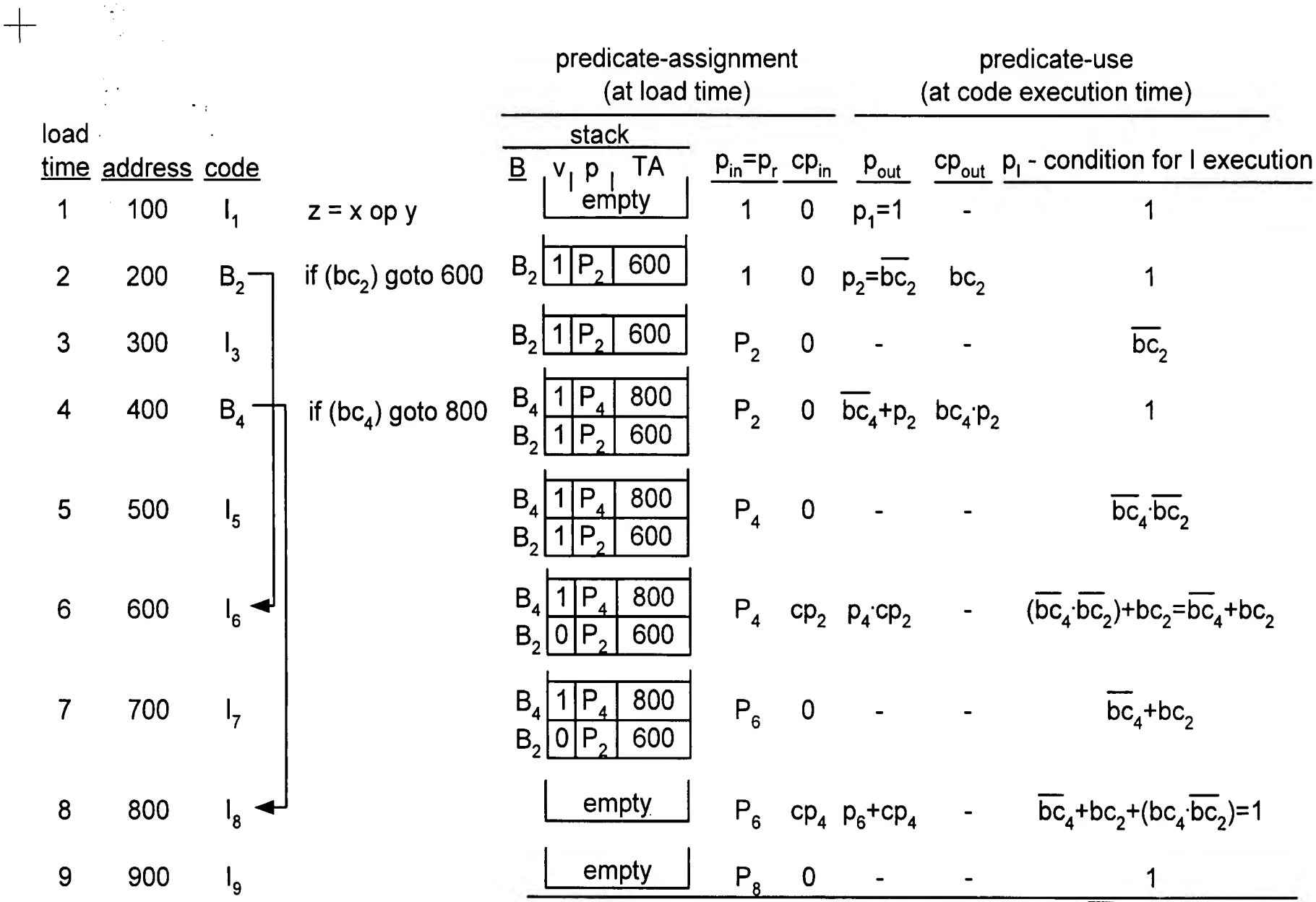

FIG. 5

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load time	address	code		predicate-assignment (at load time): stack (B v p TA)	$p_{in}=p_r$	cp_{in}	predicate-use (at code execution time): p_{out}	cp_{out}	p_i - condition for I execution
1	100	I_1	z = x op y	empty	1	0	$p_1=1$	-	1
2	200	B_2	if (bc_4) goto 800	B_2 1 P_2 1000	1	0	$p_2=\overline{bc}_2$	bc_2	1
3	300	I_3		B_2 1 P_2 1000	P_2	0	-	-	$\overline{bc}_2$
4	400	B_4	if (bc_4) goto 800	B_4 1 P_4 800 B_2 1 P_2 1000	P_2	0	$\overline{bc}_4+p_2$	$bc_4 \cdot p_2$	1
5	500	I_5		B_4 1 P_4 800 B_2 1 P_2 1000	P_4	0	-	-	$\overline{bc}_4 \cdot \overline{bc}_2$
6	600	B_6	if (bc_6) goto 1200	B_6 1 P_6 1200 B_4 1 P_4 800 B_2 1 P_2 1000	P_4	0	$\overline{bc}_6 \cdot p_4$	$bc_6 \cdot p_4$	1
7	700	I_7		B_6 1 P_6 1200 B_4 1 P_4 800 B_2 1 P_2 1000	P_6	0	-	-	$\overline{bc}_6 \cdot \overline{bc}_4 \cdot \overline{bc}_2$
8	800	I_8		B_6 1 P_6 1200 B_4 0 P_4 800 B_2 1 P_2 1000	P_6	cp_4	p_6+cp_4	-	$(\overline{bc}_6 \cdot \overline{bc}_4 \cdot \overline{bc}_2)+(bc_4 \cdot \overline{bc}_2)$ $=(\overline{bc}_6+bc_4)\overline{bc}_2$
9	900	I_9		B_6 1 P_6 1200 B_4 0 P_4 800 B_2 1 P_2 1000	P_8	0	-	-	$(\overline{bc}_6+bc_4)\overline{bc}_2$
10	1000	I_{10}		B_6 1 P_6 1200	P_8	cp_2	p_8+cp_2	-	$((\overline{bc}_6+bc_4)\overline{bc}_2)+bc_2$ $=\overline{bc}_6+bc_4+bc_2$
11	1100	I_{11}		B_6 1 P_6 1200	P_{10}	0	-	-	$(\overline{bc}_6+bc_4)bc_2$
12	1200	I_{12}		empty	P_{10}	cp_6	$p_{10}+cp_6$	-	$bc_6+bc_4+bc_2+$ $(bc_6 \cdot bc_4 \cdot bc_2)=1$
13	1300	I_{13}		empty	P_{12}	0	-	-	1

Equations - for "T": $p_1=p_{out}=p_{in}+cp_{in}$; for "B": $p_{out}=\overline{bc} \cdot p_{in}$; $cp_{out}=bc \cdot p_{in}$

FIG. 6